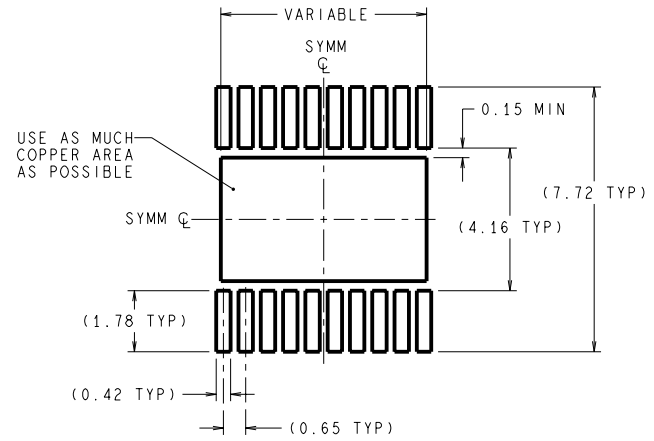
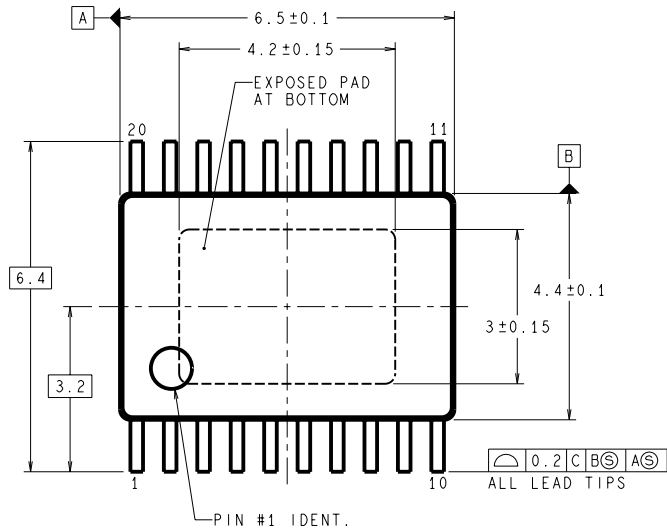
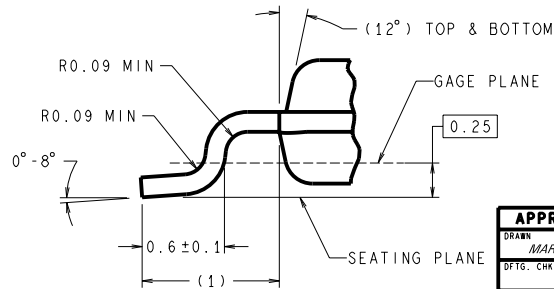
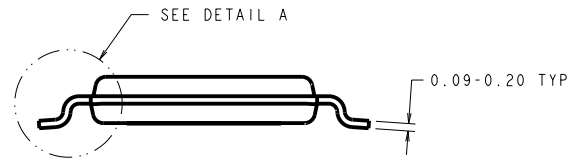
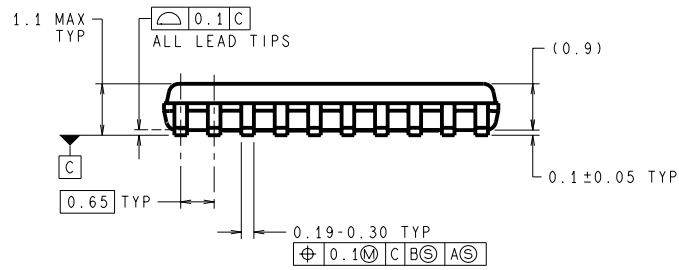


REVISIONS				
LTR	DESCRIPTION	E.C.N.	DATE	BY/APP'D
A	RELEASE TO DOCUMENT CONTROL	11919	01/26/1998	MS/



LAND PATTERN RECOMMENDATION



DIMENSIONS ARE IN MILLIMETERS

NOTES: UNLESS OTHERWISE SPECIFIED

1. REFERENCE JEDEC REGISTRATION MO-153, VARIATION ACT, DATED 10/97.

DETAIL A
TYPICAL, SCALE: 40X

APPROVALS		DATE	National Semiconductor		
DRAWN	MARTA SUCHY	01/26/1998	2900 Semiconductor dr., Santa Clara, CA 95052-8090		
DFTG. CHK.			MOLDED TSSOP, JEDEC, EXP PAD, 6.5x4.4x0.9mm BODY, 20 LD, 0.65mm PITCH		
ENGR. CHK.					
PROJECTION		SCALE	SIZE	DRAWING NUMBER	REV
		N/A	C	(SC)MKT-MXA20A	A
DO NOT SCALE DRAWING			SHEET 1 of 1		